

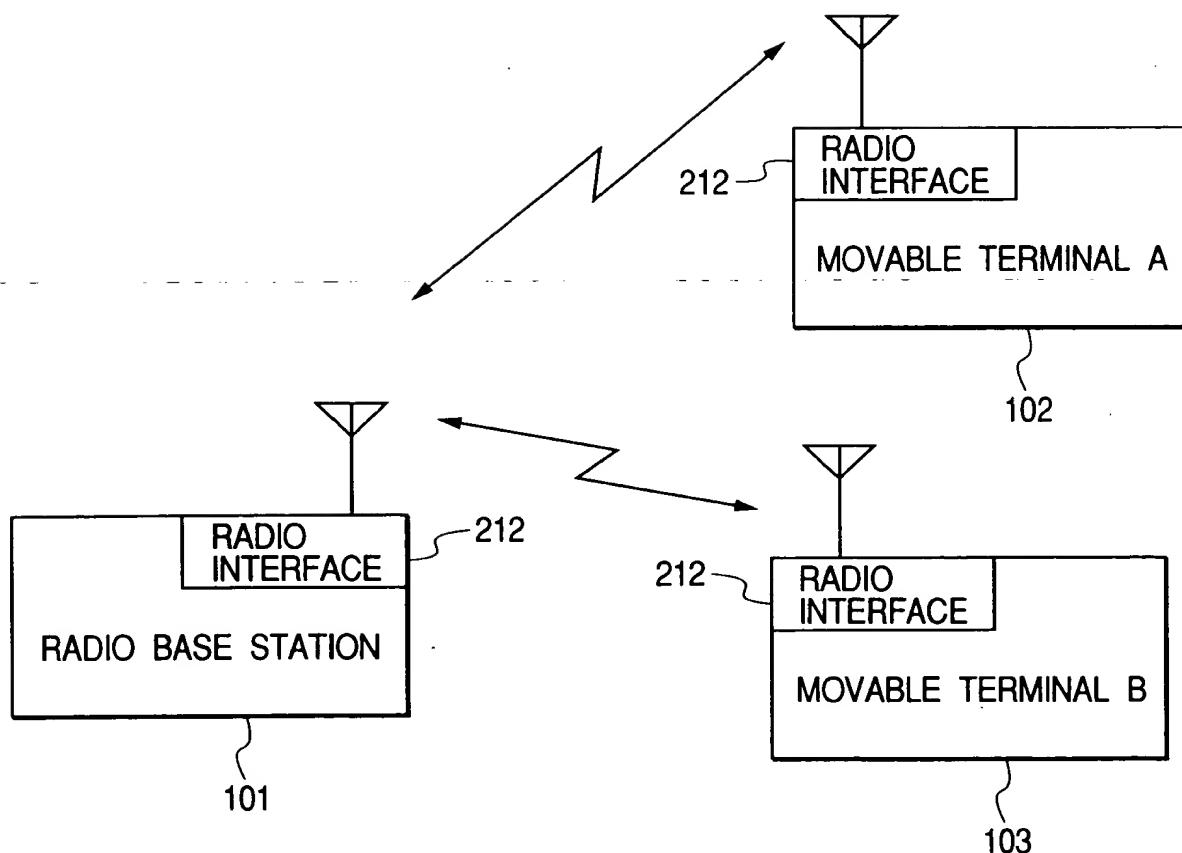
FIG. 1

FIG. 2

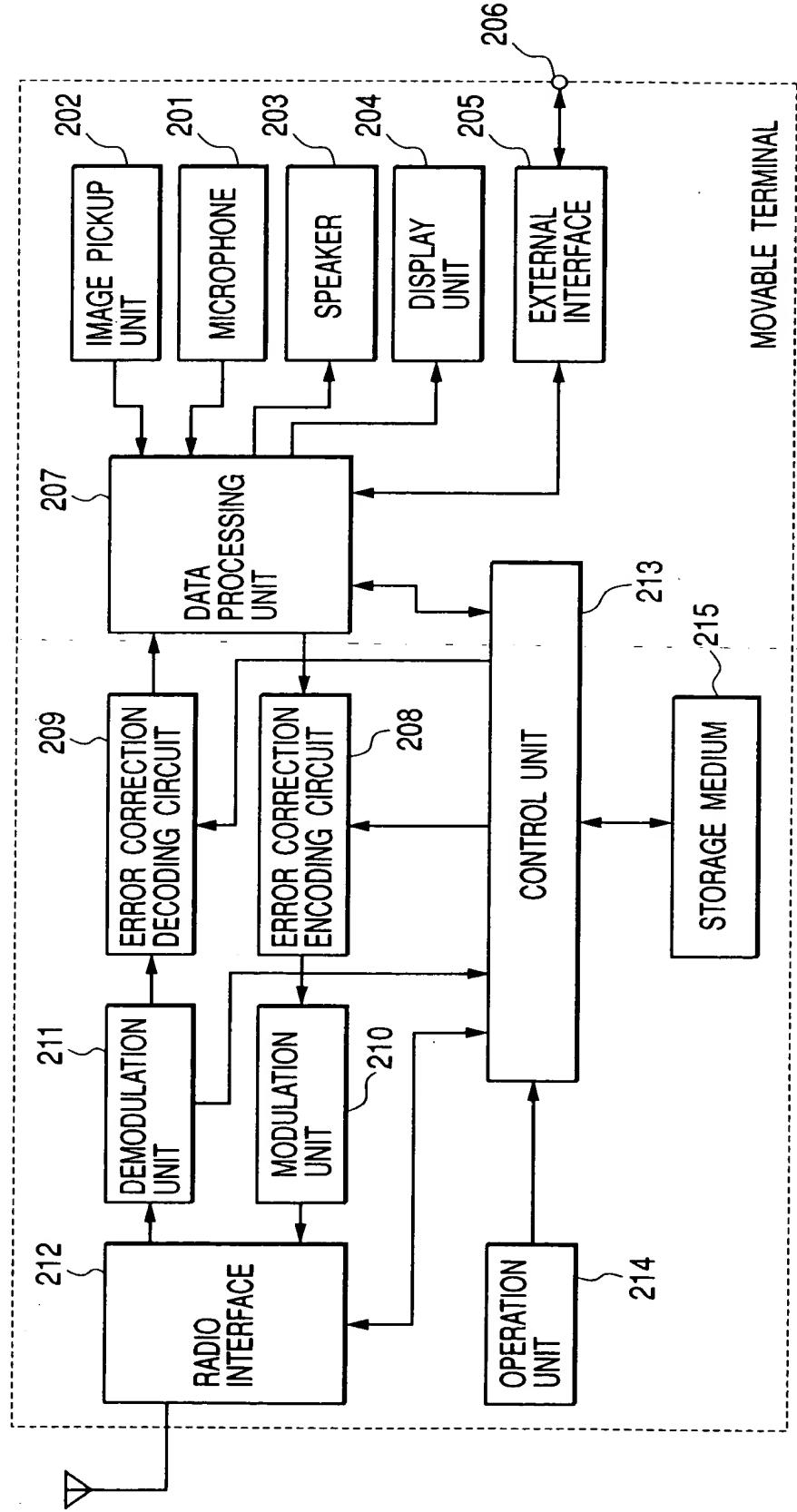


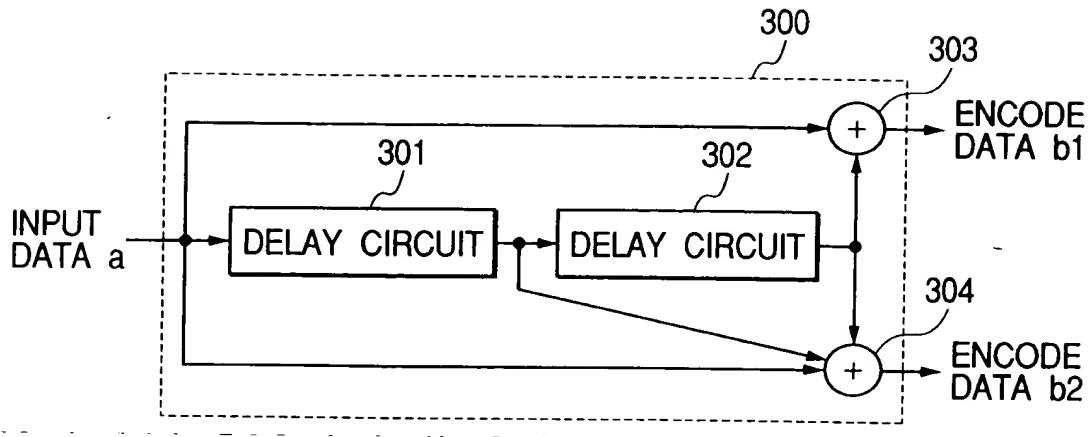
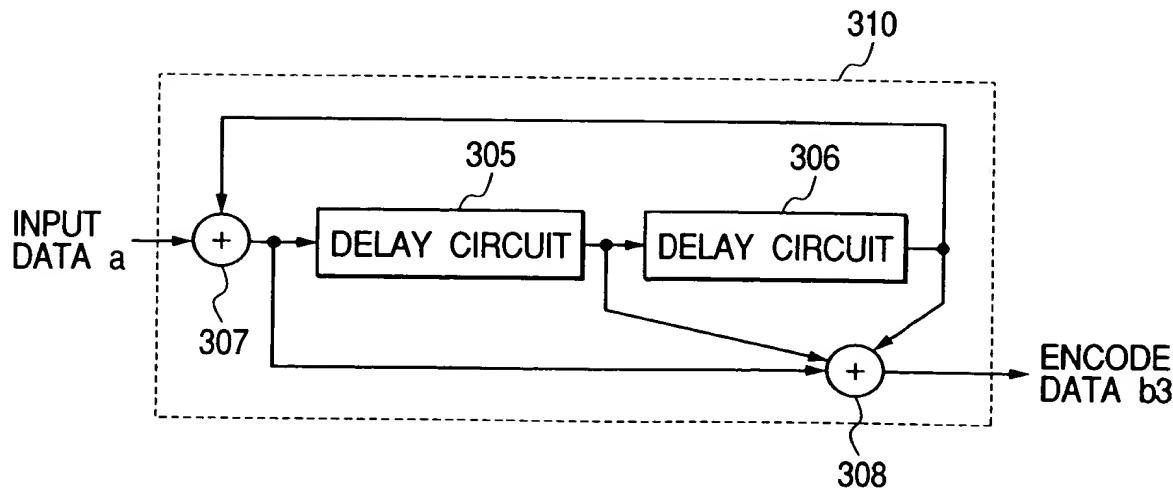
FIG. 3A***FIG. 3B***

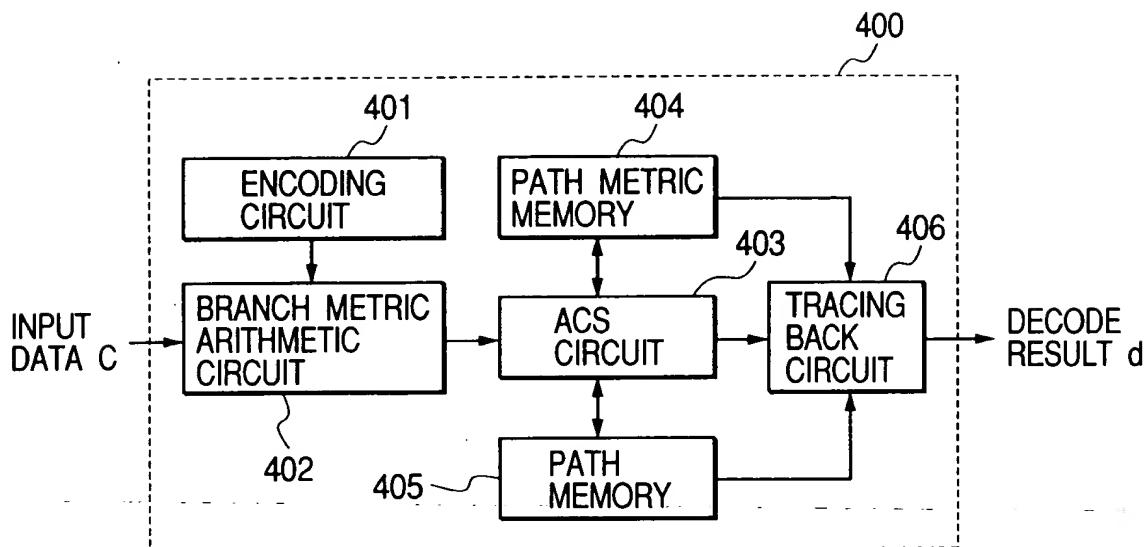
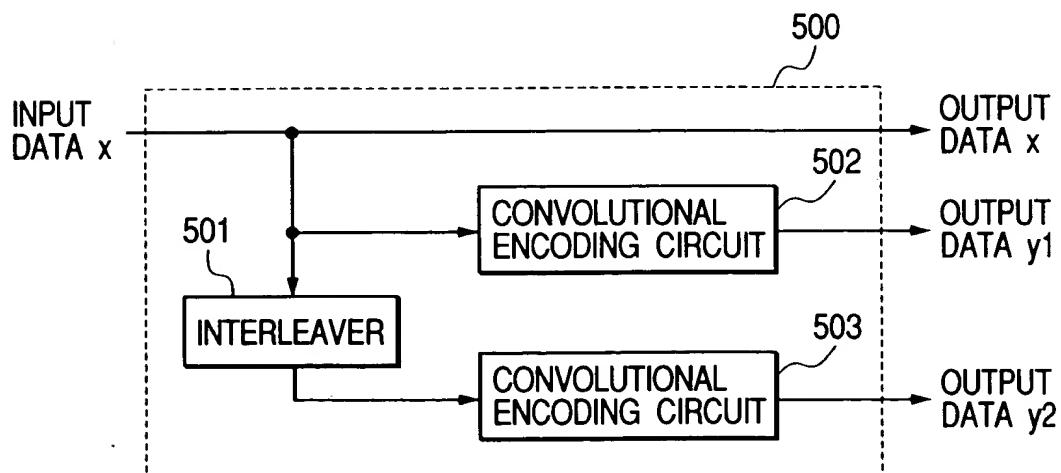
FIG. 4**FIG. 5**

FIG. 6

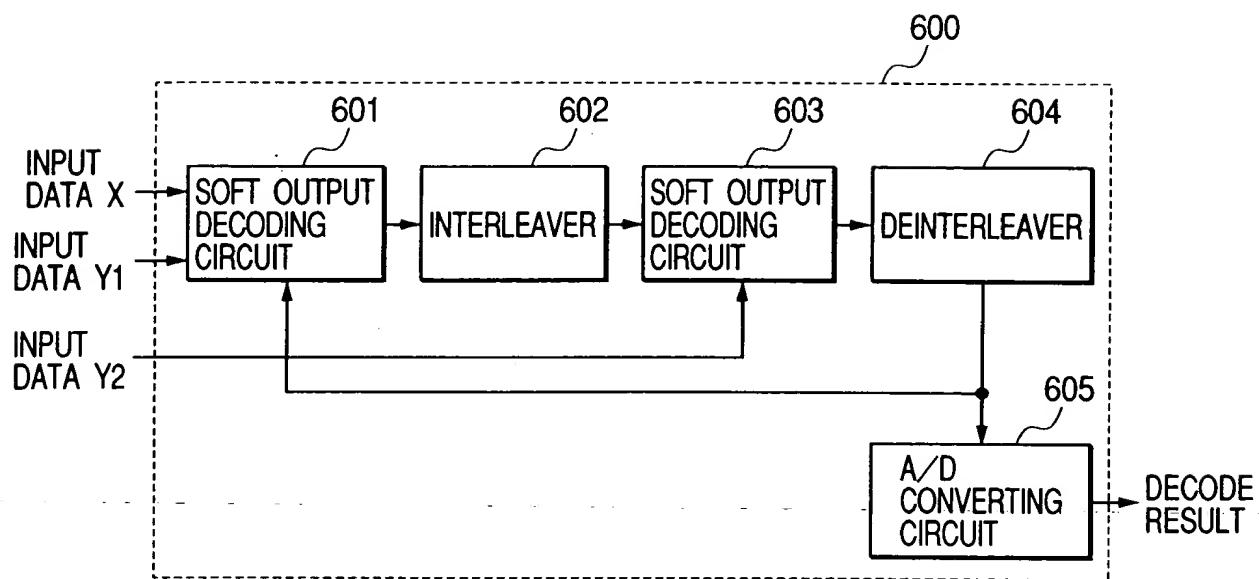


FIG. 7

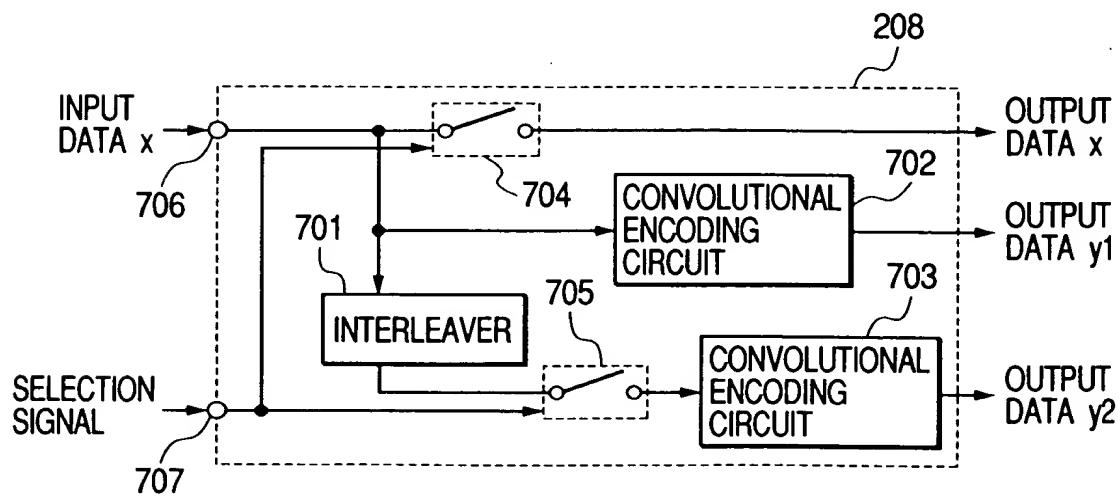


FIG. 8

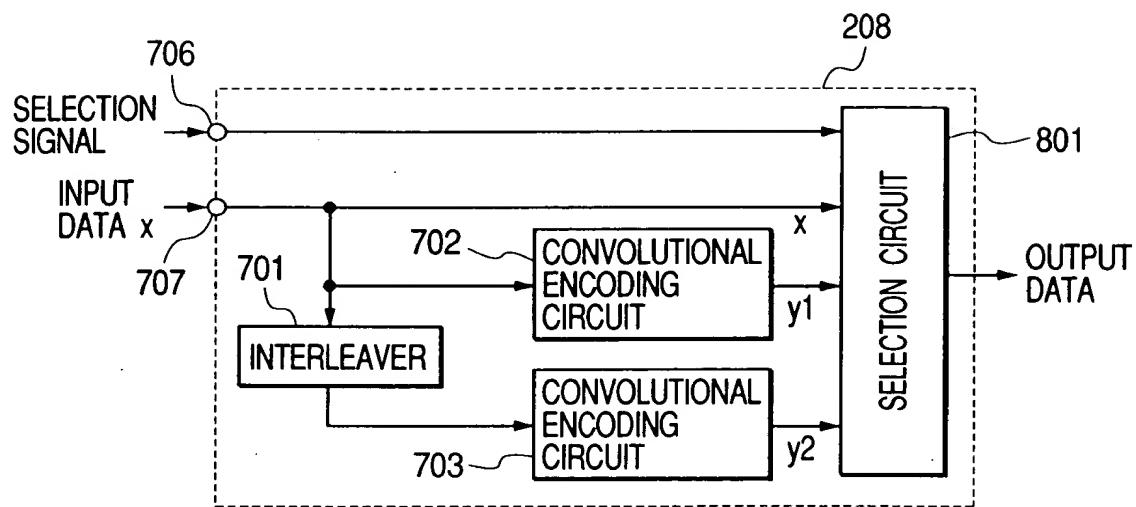


FIG. 9

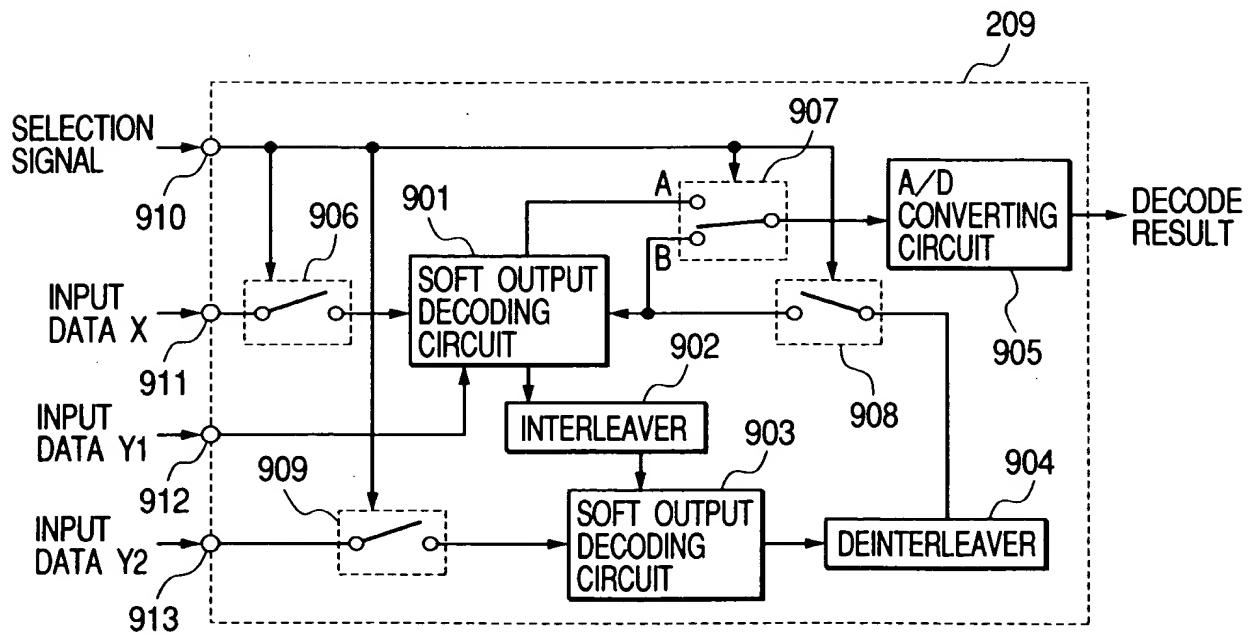


FIG. 10

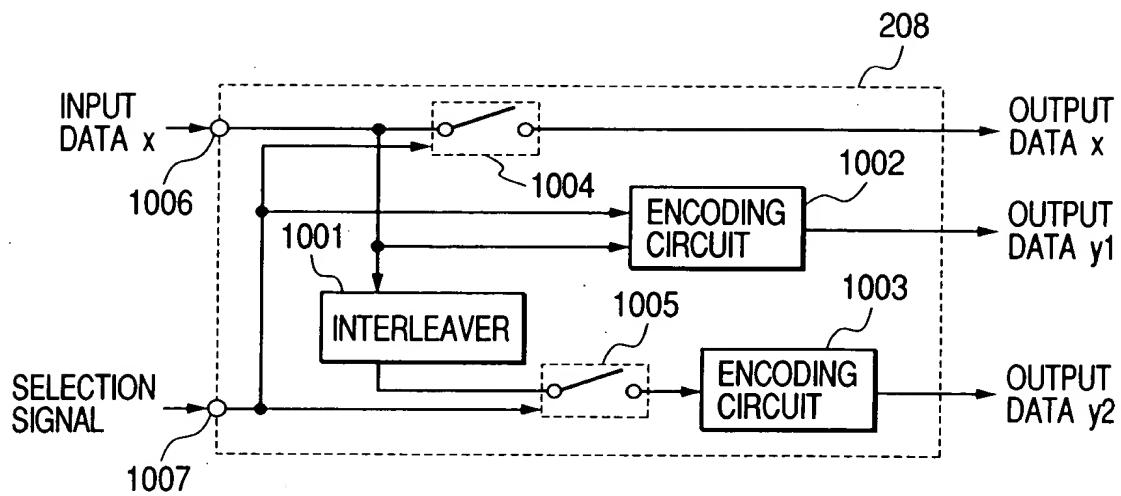


FIG. 11

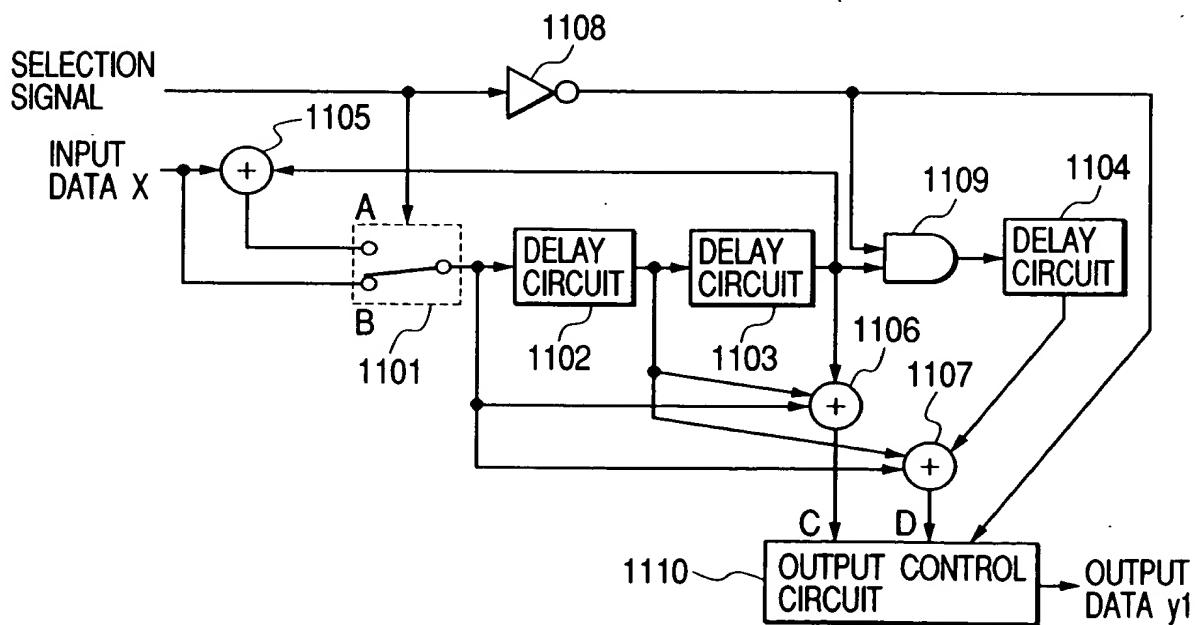


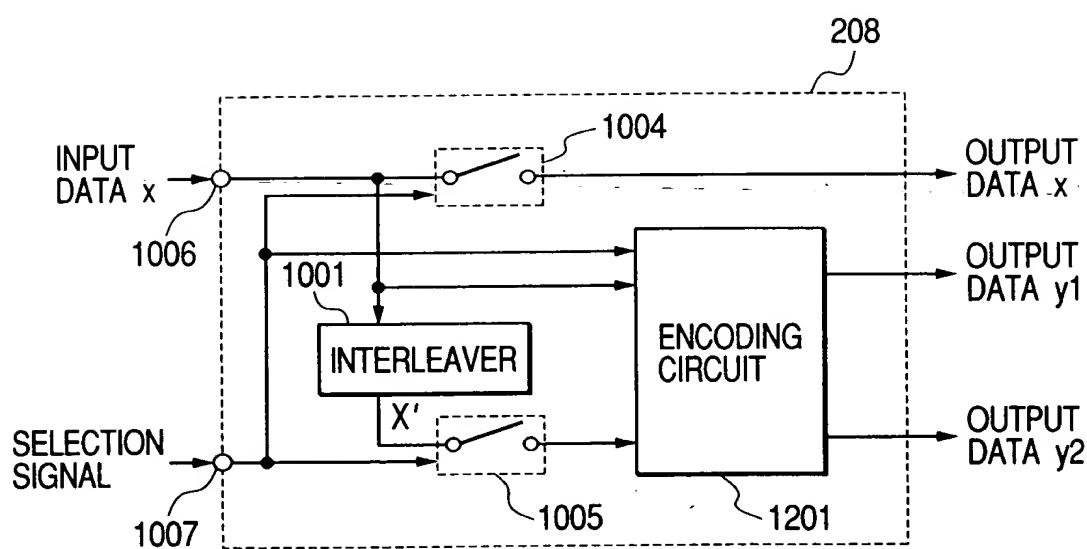
FIG. 12

FIG. 13

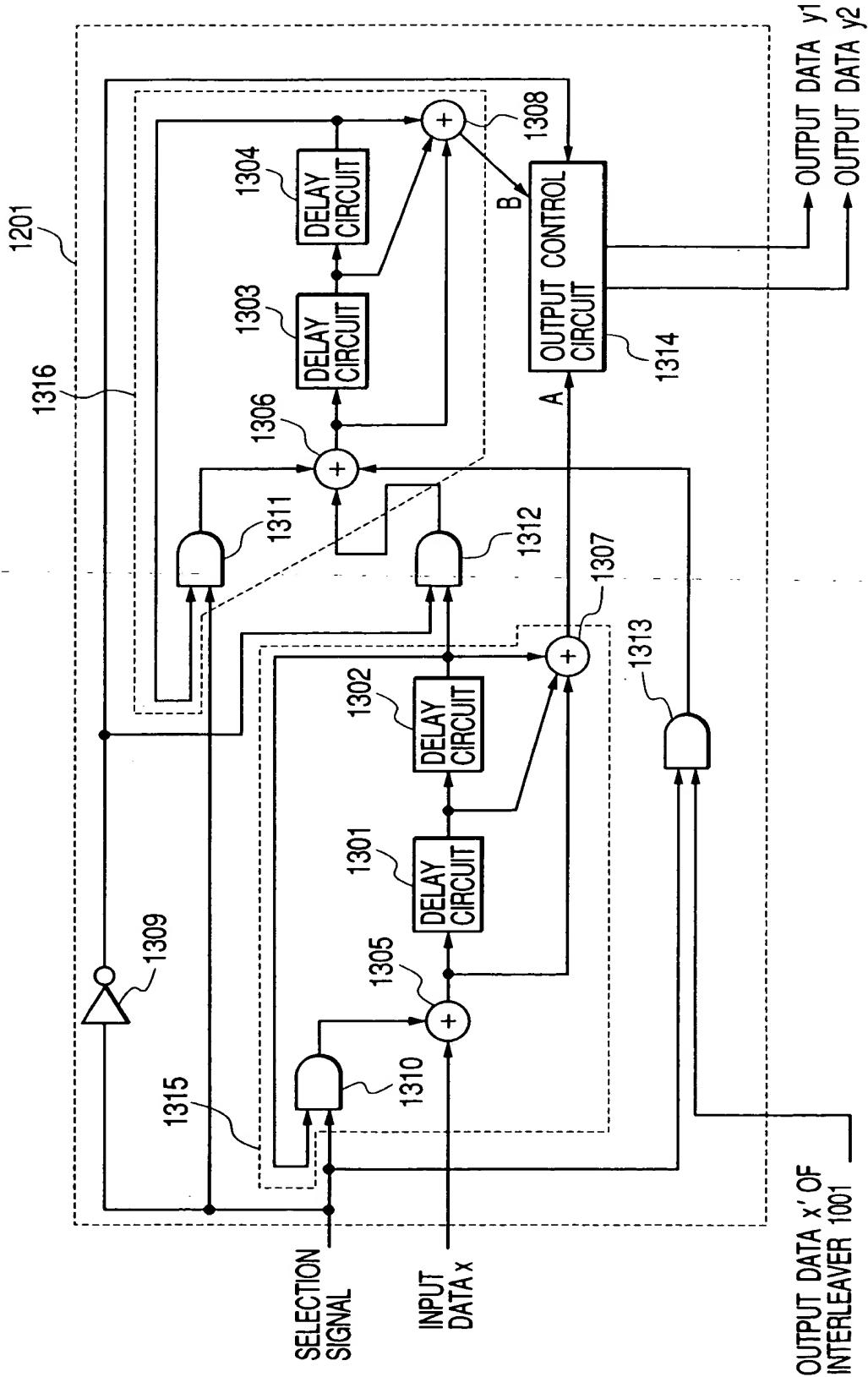


FIG. 14

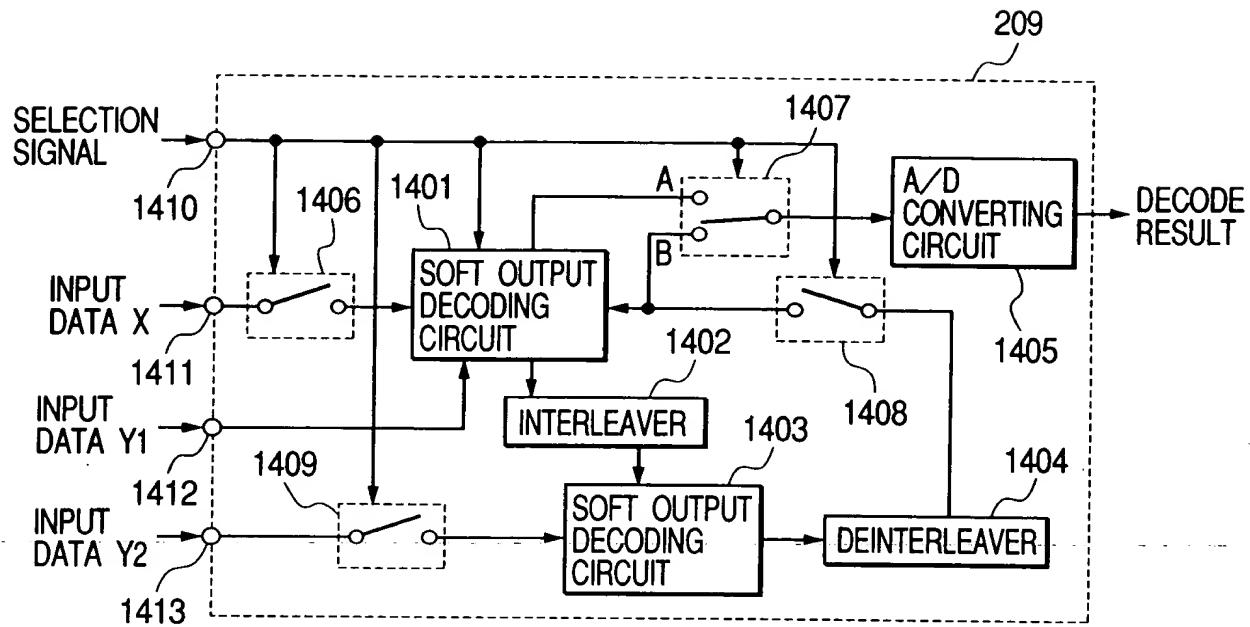


FIG. 15

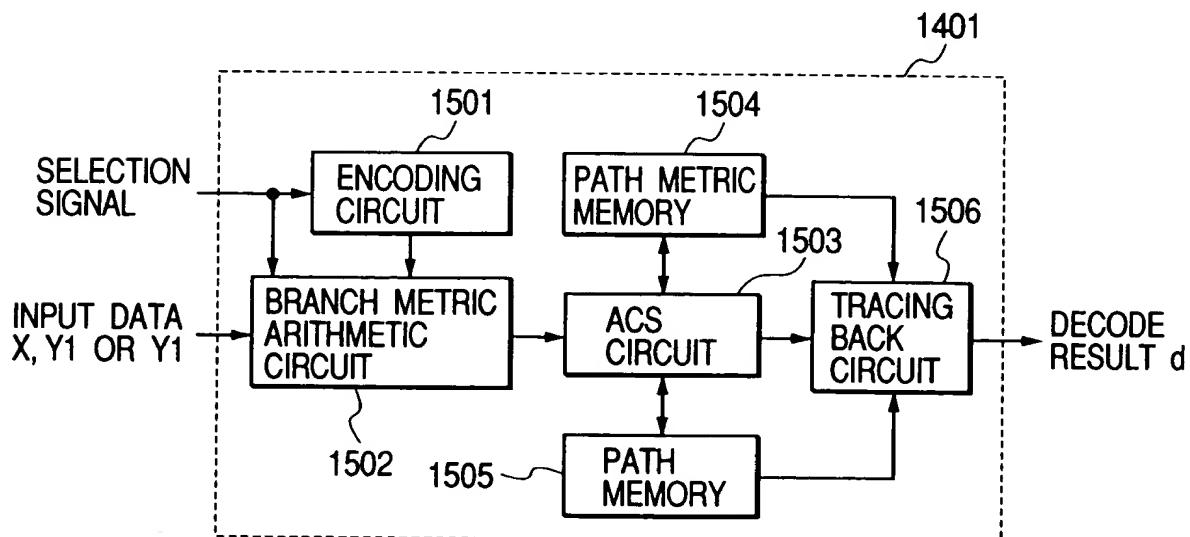


FIG. 16

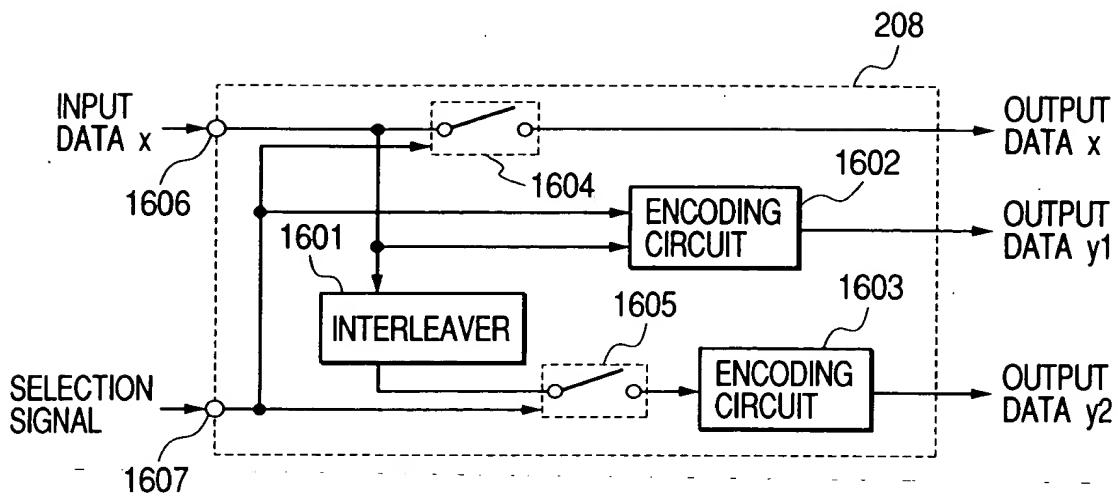


FIG. 17

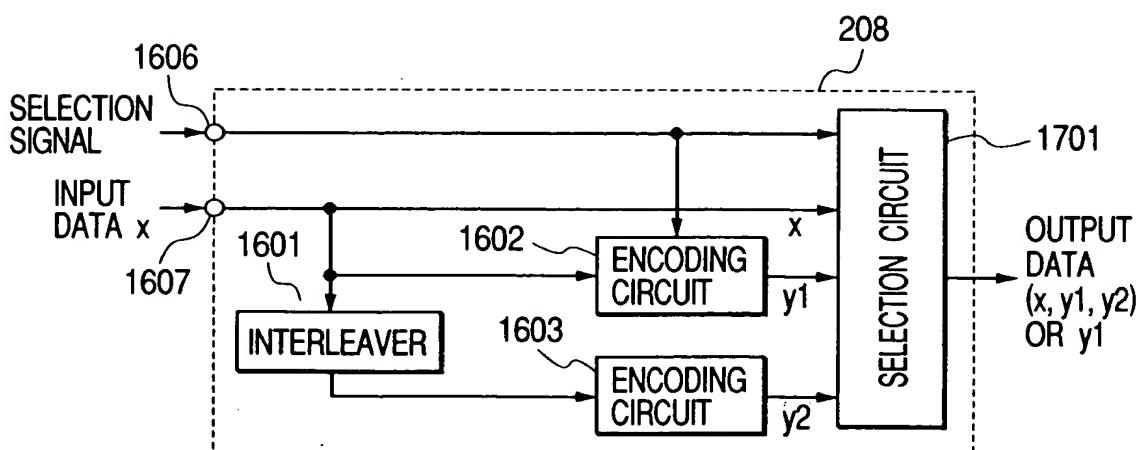
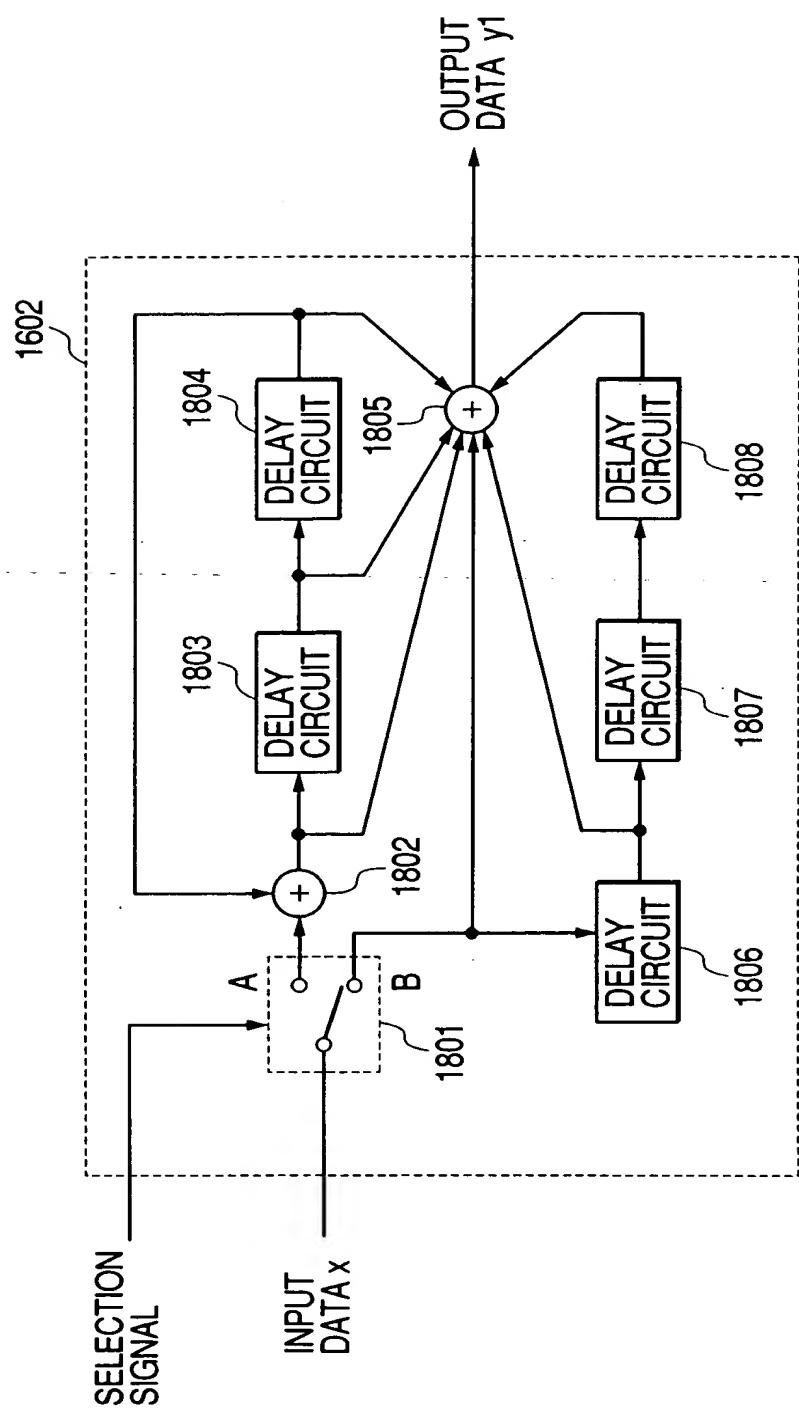


FIG. 18



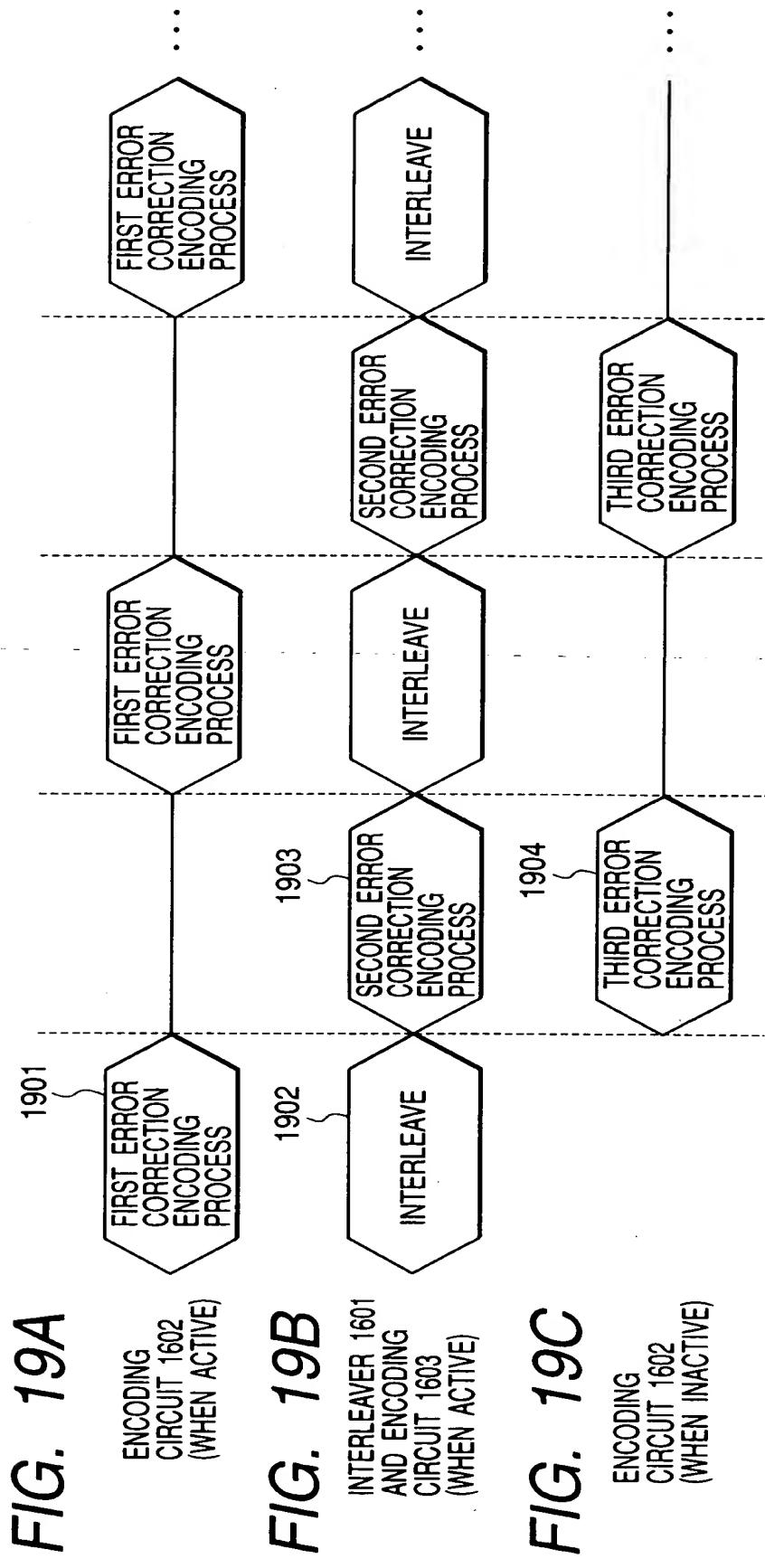


FIG. 20

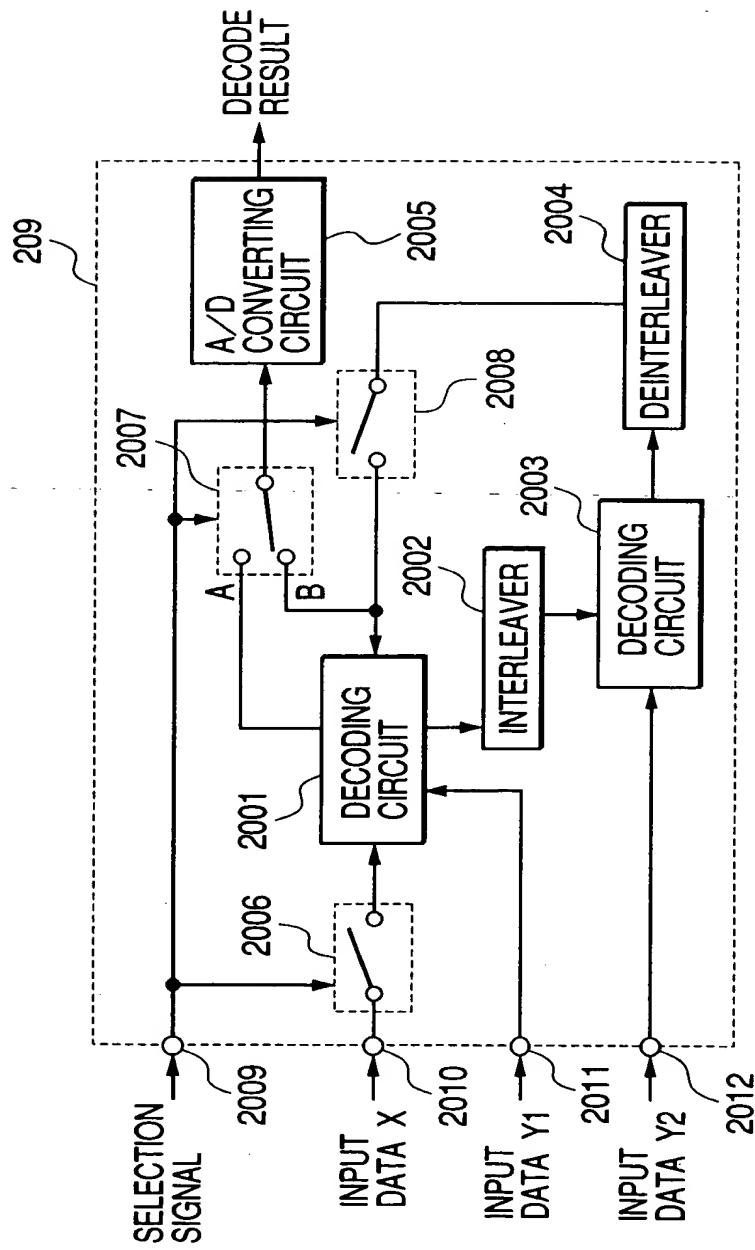


FIG. 21

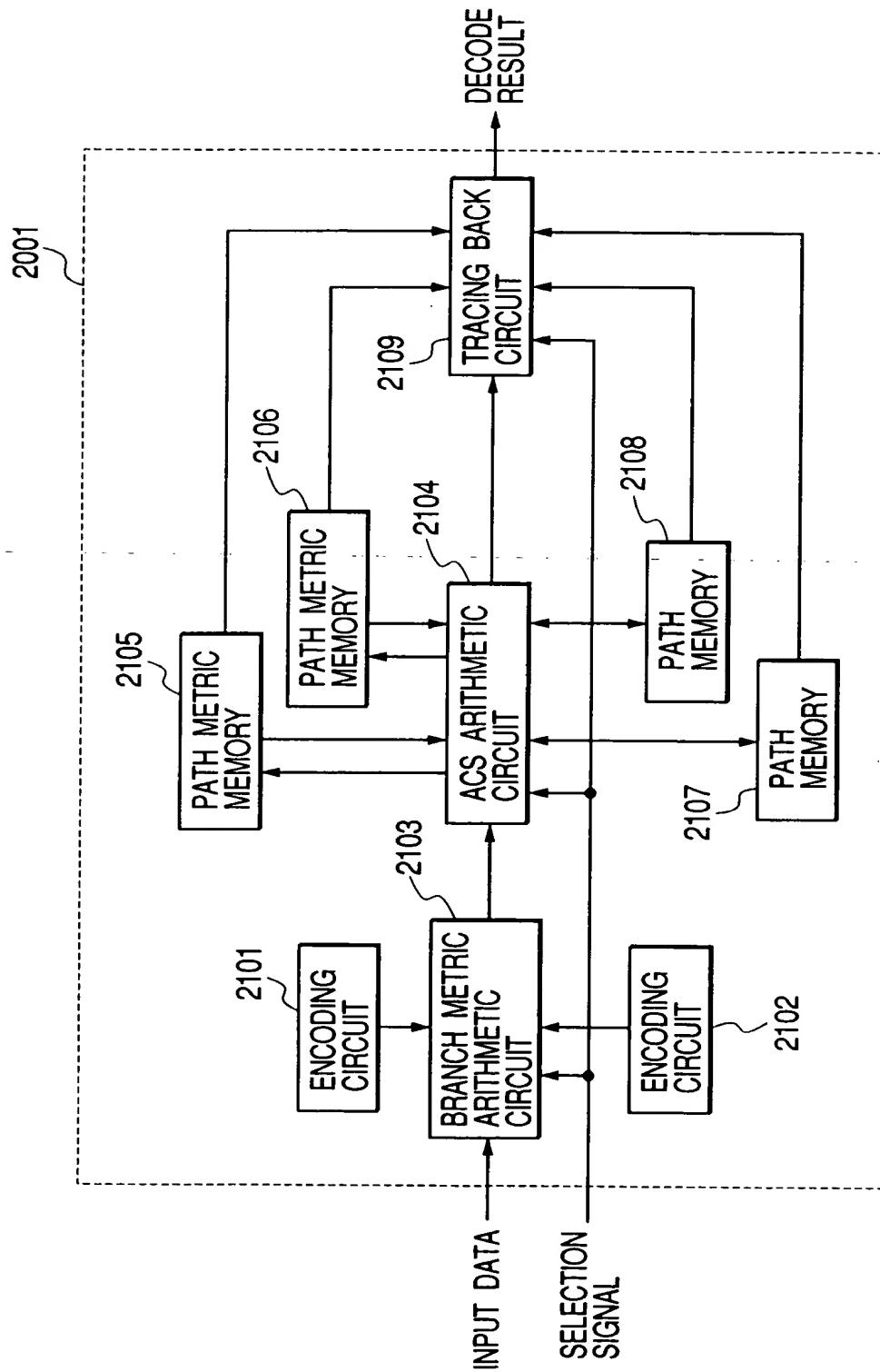
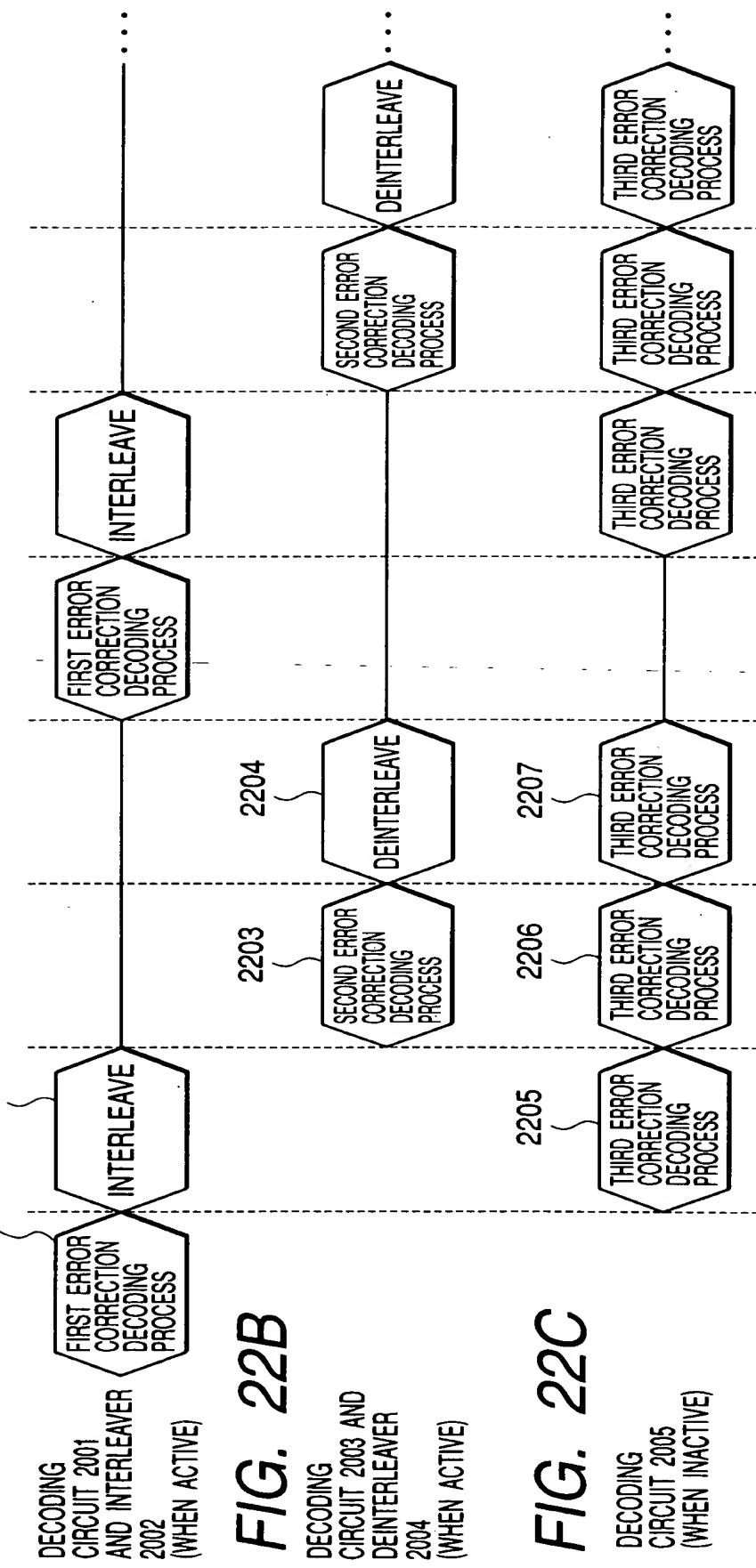


FIG. 22A**FIG. 22B**

DECODING CIRCUIT 2003 AND DEINTERLEAVER 2004 (WHEN ACTIVE)

FIG. 22C

DECODING CIRCUIT 2005 (WHEN INACTIVE)

FIG. 23

2001

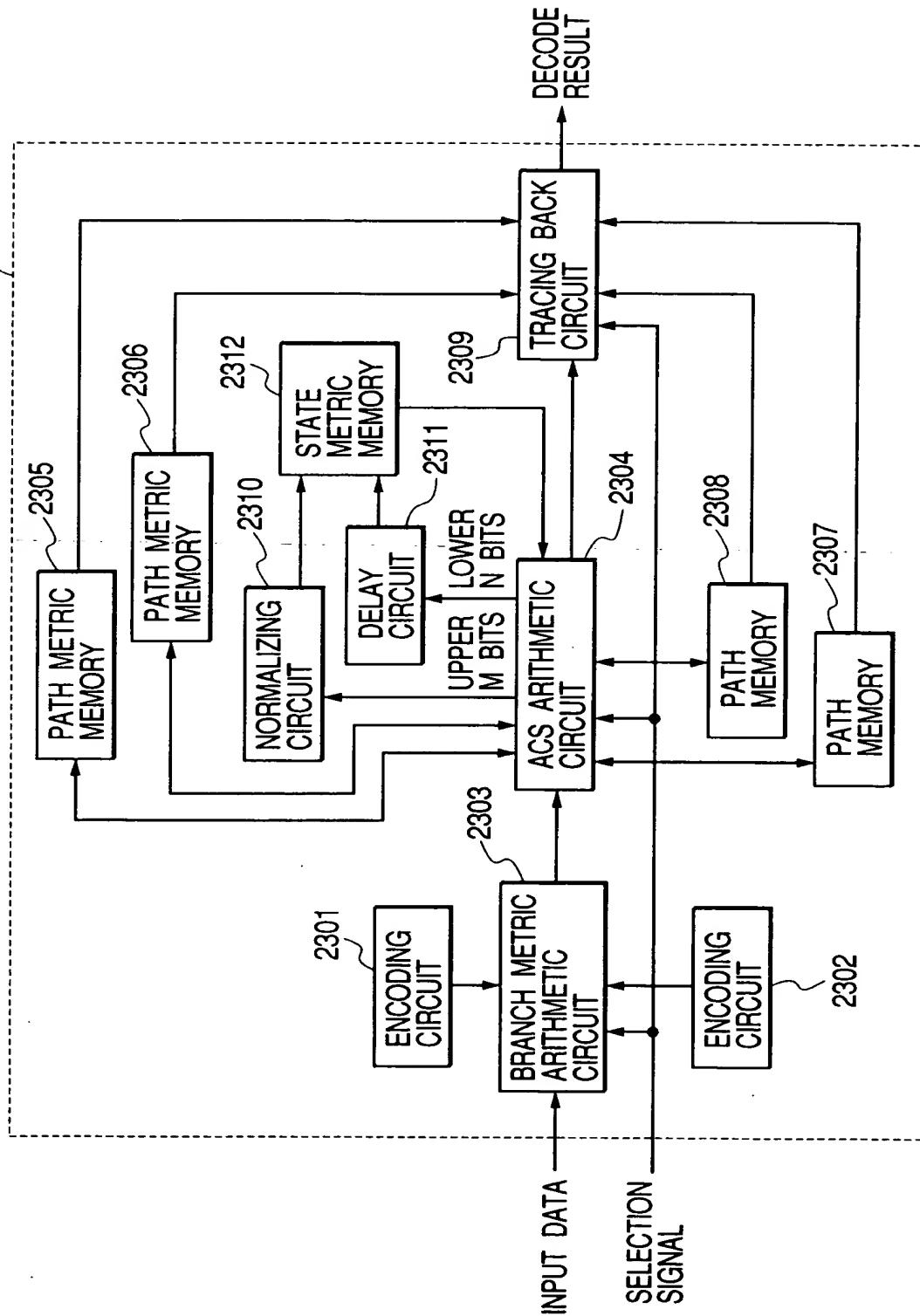


FIG. 24

